L Number	Hits	Search Text	DB	Time stamp
1	59	map$4 with (cache$1 with (memory adj line))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:17
2	72	(cache$1 with (memory adj line)) with (location$ or postion$1)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:19
3	315	(711/129).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:19
4	2	((cache$1 with (memory adj line)) with (location$ or postion$1)) and ((711/129).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:23
5	25	((determin$5 or assign$3 or defin$3 or decid$3) near10 (position$1 or location$1) near10 (memory adj line))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:25
6	11	map$4 near10 ((line$1 or slice$1 or section$1) near2 memory) with (cache near3 (section$1 or partition$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:29
7	61	(determin$5 or assign$3 or defin$3 or decid$3 or allocat$3) near10 ((line$1 or slice$1 or section$1) near2 memory) with (cache near3 (section$1 or partition$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:30
8	64	(accord$3 or base$1) with ((location$1 or postion$1) near5 ((line or slice or section) near2 memory))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:32
9	11	((711/129).CCLS.) and ((determin$5 or assign$3 or defin$3 or decid$3 or allocat$3) near10 ((line$1 or slice$1 or section$1) near2 memory) with (cache near3 (section$1 or partition$1)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:33
11	1	((711/129).CCLS.) and ((accord$3 or base$1) with ((location$1 or postion$1) near5 ((line or slice or section) near2 memory)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/17 13:33
-	315	(711/129).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 11:16
-	285	((711/129).CCLS.) and @ad<=20011109	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 14:48
-	233	((711/129).CCLS.) and (cach$3 near10 (location$1 or place$1 or address$2))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 11:18
-	216	(((711/129).CCLS.) and (cach$3 near10 (location$1 or place$1 or address$2))) and @ad<=20011109	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 11:18
-	7	((711/129).CCLS.) and (cach$3 near10 (location$1 or place$1 or address$2)) and (allocat$3 near5 policy)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 14:47

-		7	((711/129).CCLS.) and (cach$3 near10 (location$1 or place$1 or address$2)) and (allocat$3 near5 (policy or rule$1))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 11:32
-		212	allocation adj (polity or rule)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 11:33
-		0	((711/129).CCLS.) and (allocation adj (polity or rule))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 11:33
-		77	(allocation near5 (policy or rule)) same cach$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 13:22
-		14	(allocat$3 near5 based near5 location$1) same cach$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 13:34
-		606	(memory near5 location$1) near10 determin$5 near10 cach$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 13:35
-		18	((711/129).CCLS.) and ((memory near5 location$1) near10 determin$5 near10 cach$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 14:02
-		346	(address$2 near3 memory) near10 based near10 cach$3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 14:05
-		12	((711/129).CCLS.) and ((address$2 near3 memory) near10 based near10 cach$3)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 14:05
-		24	dynamic$4 near5 (adjust$3 or vary$3) near5 size near5 cache	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 14:44
-		192	((711/129).CCLS.) and ((based or determin$3) near10 (location$2 or address$2))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 14:58
-		177	(((711/129).CCLS.) and ((based or determin$3) near10 (location$2 or address$2))) and @ad<=20011109	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 14:48
-		34	(partition near3 cache) same ((based or determin$3) near10 (location$2 or address$2))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 15:14
-		749	(711/133).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 15:14
-		167	(711/134).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 15:15

-	339	(711/153).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 15:15
-	386	(711/159).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 15:15
-	1314	(711/170).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 15:15
-	703	(711/173).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 15:15
-	1410	(711/118).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 15:17
-	19	((((711/129).CCLS.) and ((based or determin$3) near10 (location$2 or address$2))) and @ad<=20011109) and ((711/133).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 15:17
-	7	((((711/129).CCLS.) and ((based or determin$3) near10 (location$2 or address$2))) and @ad<=20011109) and ((711/134).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 15:17
-	18	((((711/129).CCLS.) and ((based or determin$3) near10 (location$2 or address$2))) and @ad<=20011109) and ((711/153).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 15:17
-	4	((((711/129).CCLS.) and ((based or determin$3) near10 (location$2 or address$2))) and @ad<=20011109) and ((711/159).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 15:17
-	12	((((711/129).CCLS.) and ((based or determin$3) near10 (location$2 or address$2))) and @ad<=20011109) and ((711/170).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 15:18
-	34	((((711/129).CCLS.) and ((based or determin$3) near10 (location$2 or address$2))) and @ad<=20011109) and ((711/173).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 15:18
-	18	((((711/129).CCLS.) and ((based or determin$3) near10 (location$2 or address$2))) and @ad<=20011109) and ((711/118).CCLS.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/09/14 15:18

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1 **A dynamic programming algorithm for cache memory partitioning fɩ real-time systems**
Sasinowski, J.E.; Strosnider, J.K.;
Computers, IEEE Transactions on , Volume: 42 , Issue: 8 , Aug. 1993
Pages:997 - 1001

[Abstract]   [PDF Full-Text (416 KB)]   **IEEE JNL**

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1 **Data cache sizing for embedded processor applications**
Panda, P.R.; Nicolau, N.D.; Nicolau, A.;
Design, Automation and Test in Europe, 1998., Proceedings , 23-26 Feb. 199;
Pages:925 - 926

[Abstract]   [PDF Full-Text (32 KB)]   **IEEE CNF**

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2 **A modified approach to data cache management**
Tyson, G.; Farrens, M.; Matthews, J.; Pleszkun, A.R.;
Microarchitecture, 1995. Proceedings of the 28th Annual International Sympo
on , 29 Nov.-1 Dec. 1995
Pages:93 - 103

[Abstract]   [PDF Full-Text (1084 KB)]   **IEEE CNF**

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3 **Partitioning regular grid applications with irregular boundaries for cache-coherent multiprocessors**
Yang Zeng; Abraham, S.G.;
Parallel Processing Symposium, 1995. Proceedings., 9th International , 25-2£
1995
Pages:222 - 228

[Abstract]   [PDF Full-Text (860 KB)]   **IEEE CNF**

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4 **An argument for simple COMA**
Saulsbury, A.; Wilkinson, T.; Carter, J.; Landin, A.;
High-Performance Computer Architecture, 1995. Proceedings. First IEEE
Symposium on , 22-25 Jan. 1995
Pages:276 - 285

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[Abstract]   [PDF Full-Text (732 KB)]   **IEEE CNF**

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5 Reducing branch delay to zero in pipelined processors
Gonzalez, A.M.; Llaberia, J.M.;
Computers, IEEE Transactions on , Volume: 42 , Issue: 3 , March 1993
Pages:363 - 371

[Abstract]   [PDF Full-Text (760 KB)]   **IEEE JNL**

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6 Reducing power consumption for high-associativity data caches in embedded processors
Nicolaescu, D.; Veidenbaum, A.; Nicolau, A.;
Design, Automation and Test in Europe Conference and Exhibition, 2003 , 200
Pages:1064 - 1068

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